

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

UEDA

Atty. Ref.: 4034-5

Serial No. to be assigned

Group: unknown

Filed: December 18, 2001

Examiner: unknown

For: THIN-FILM TRANSISTOR, METHOD FOR FABRICATING THE SAME, AND
LIQUID CRYSTAL DISPLAY DEVICE

* * * * *

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

PRELIMINARY AMENDMENT

In order to place the above-identified application in better condition for
examination, please amend the application as follows:

IN THE CLAIMS

Please amend claim 23 as follows:

23. {AMENDED} An active-matrix-addressed liquid crystal display device
comprising:

a substrate, on which the thin-film transistor according to claim 1; a data bus line
electrically connected to the first heavily doped region of the thin-film transistor; a gate
bus line electrically connected to at least one of the gate electrodes of the thin-film
transistor; and a pixel electrode electrically connected to the second heavily doped region
of the thin-film transistor have been formed, and

UEDA

Serial No. to be assigned

a liquid crystal layer, which has an optical state changeable with a potential level at the pixel electrode.

Please add new claim 24 as follows:

24. {NEW} An active-matrix-addressed liquid crystal display device comprising:
a substrate, on which the thin-film transistor according to claim 14; a data bus line electrically connected to the first heavily doped region of the thin-film transistor; a gate bus line electrically connected to at least one of the gate electrodes of the thin-film transistor; and a pixel electrode electrically connected to the second heavily doped region of the thin-film transistor have been formed, and

a liquid crystal layer, which has an optical state changeable with a potential level at the pixel electrode.

REMARKS

The above amendments are made to place the claims in a more traditional format.

Attached hereto is a marked-up version of the changes made to the claims by the current amendment. The attached page(s) is captioned "Version With Markings To Show Changes Made."

Respectfully submitted,

NIXON & VANDERHYE P.C.

December 18, 2001

By: 

H. Warren Burnam, Jr.
Reg. No. 29,366

HWB:lsh
1100 North Glebe Road, 8th Floor
Arlington, VA 22201-4714
Telephone: (703) 816-4000
Facsimile: (703) 816-4100

VERSION WITH MARKINGS TO SHOW CHANGES MADE**IN THE CLAIMS**

23. {AMENDED} An active-matrix-addressed liquid crystal display device comprising:

a substrate, on which the thin-film transistor according to claim 1 [or 14]; a data bus line electrically connected to the first heavily doped region of the thin-film transistor; a gate bus line electrically connected to at least one of the gate electrodes of the thin-film transistor; and a pixel electrode electrically connected to the second heavily doped region of the thin-film transistor have been formed, and

a liquid crystal layer, which has an optical state changeable with a potential level at the pixel electrode.